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\u0026 JTAG

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Boundary scan (JTAG) is a powerful testing scheme that is widely used in nowadays circuits to maintain and verify operation of the hardware. However, JTAG is not used in cryptographic hardware since it may be used to compromise security of the implemented cryptographic algorithm. This paper analyses different solutions proposed to overcome the threat of such attacks, presents requirements that have to be satisfied in order to construct effective security solution, and presents novel proposal ...

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Boundary Scan Security Enhancements for a Cryptographic Hardware. Share on. Author: Maciej Nikodem. The Institute of Computer Engineering,

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Control and Robotics, Wroc ł aw University of Technology, Wroc ł aw, Poland 50-370 .

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Series of In-System device is the only way to reset the read security bit Table 3: Data ...

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