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Xeon processors will use high-bandwidth memory (HBM) to speed processing, with Intel also announcing Ponte Vecchio GPU validation, Ethernet HPC use, and commercial support for DAOS object storage.

Sapphire Rapids Xeons with HBM could let DAOS reign

Abraham prize for for their paper, Adaptive Federated Learning in Resource Constrained Edge Computing ... of the lab's Distributed Analytics and Information Science International Technology ...

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HPE details the future for edge-to-cloud at Discover 2021
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