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...

8085 is pronounced
as "eighty-eighty-
five" microprocessor.
It is an 8-bit

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Microprocessor
designed by Intel in
1977 using NMOS
technology. It has the
following
configuration - 8-bit
data bus; 16-bit
address bus, which
can address upto
64KB; A 16-bit
program counter; A
16-bit stack pointer;
Six 8-bit registers
arranged in pairs: BC,

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Penram International
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Page 97 Section 4.1.

Page 99 . Page 103 .

Page 32 . Instruction

Set Consists of 74

operation codes

resulting in 246

instructions ($< 2^8$)

2

8085 - E & ICT

Academy

An output device is
interfaced with 8-bit
microprocessor

8085A. The

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interfacing circuit is shown in figure. The interfacing circuit makes use of 3 Line to 8 Line decoder having 3 enable lines E_1 , E_2 , E_3 . The address of the device is

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8085 Microprocessor:

Ans. 8085 has 8 data lines and 16 address lines. The memory size of any microprocessor depends on the number of address lines. The general formula is 2^n , where n = number of address lines. For 8085 there are $2^{16} = 64$ Kbytes memory

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for Microprocessor

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8085 microprocessor
Trainer kit is

proposed to smooth
the progress of
learning and
developing designs
of 8085

microprocessor
family from Intel. This

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kit can be programmed using a PC ' s 101/104 Keyboard without using a pc, thus making this kit as a standalone kit. User can enter programs in Assembly languages.

8085 Lab Trainer Kit-
- Pantech Solutions
The Intel 8085 is an

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8-bit microprocessor produced by Intel and introduced in March 1976. It is a software-binary compatible with the more-famous Intel 8080 with only two minor instructions added to support its added interrupt and serial input/output features. However, it requires less support

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circuitry, allowing simpler and less expensive microcomputer systems to be built. The "5" in the part number highlighted the fact that the 8085 uses a single +5-volt power supply by using depletion-mode tr

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Answer: (d). (I) , (III) &
(IV) 5. Consider the
following statements:
In 8085

microprocessor, data-
bus and address bus
are multiplexed in
order to. I) Increase
the speed of
microprocessor.
II) Reduce the number
of pins. III) Connect
more peripheral
chips.

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Microprocessor

Multiple choice

Questions and

Answers-Intel ...

The code for 8-bit
multiplication for

Microprocessor 8085

is as follows: LHLD

8100H. XCHG. MOV C,

D. MVI D, 00H. LXI H,

0000 H. Loop: DAD D.

DCR C. JNZ Loop.

SHLD 8102 H. HLT...

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Answered: Write an assembly language program in... |

bartleby

AC23 Microprocessor
Based System Design

50 1 0 1 Display write
inhibit Allows half-

bytes to be blanked 1

1 0 Clear Clears the

display of FIFO 1 1 1

End interrupt Clears

the IRQ signal to the

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summary Q.67 State
the importance of
PUBLIC, EXTRN
directives in modular
programming.(4) Ans
The PUBLIC and
EXTRN directives are
very important to
modular
programming.

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